

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	444656	latch latching flip-flop\$1 flip adj flop\$1	USPAT; US-PGPUB ; EPO; JPO; DERWENT	2004/09/21 07:37	
2	BRS	L2	42471	1 and (latch latching flip-flop\$1 flip adj flop\$1) with (selecting selector selection select mux multiplexor multiplexer multiplexing)	USPAT; US-PGPUB ; EPO; JPO; DERWENT	2004/09/21 07:38	
3	BRS	L3	3727	2 and (latch latching flip-flop\$1 flip adj flop\$1) with (selecting selector selection select mux multiplexor multiplexer multiplexing) with (delay delayed delaying)	USPAT; US-PGPUB ; EPO; JPO; DERWENT	2004/09/21 07:41	
4	BRS	L4	3176	3 and control\$4	USPAT; US-PGPUB ; EPO; JPO; DERWENT	2004/09/21 07:40	
5	BRS	L5	2697	4 and clock	USPAT; US-PGPUB ; EPO; JPO; DERWENT	2004/09/21 07:40	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
6	BRS	L6	1627	5 and (synchronization synchronized synchronizing synchronize)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT	2004/09/21 07:41	
7	BRS	L7	1065	6 and (latch latching flip-flop\$1 flip adj flop\$1) with (delay delayed delaying) with clock	USPAT; US-P GPUB ; EPO; JPO; DERW ENT	2004/09/21 07:44	
8	BRS	L8	221	7 and (latch latching flip-flop\$1 flip adj flop\$1) with (delay delayed delaying) with clock with (synchronization synchronized synchronizing synchronize)	USPAT; US-P GPUB ; EPO; JPO; DERW ENT	2004/09/21 07:44	